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Dated: May 15, 2002

Signature:

(Marcus J. Millet)

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ORIGINALLY FILED

Docket No.: TESSERA 3.0-030 CIP V DIV CONT
(PATENT)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
DiStefano et al.

Application No. 09/863,927

Filed: May 23, 2001

For: FAN-OUT SEMICONDUCTOR CHIP
ASSEMBLY

Group Art Unit: 2822

Examiner: Lewis, M.

Commissioner for Patents
Washington, DC 20231

RESPONSE

Dear Sir:

In response to a restriction requirement mailed January 15, 2002, applicant hereby elects Group I, namely claim 2, for prosecution in this application.

If there are any additional charges in connection with this response, the Examiner is authorized to charge Deposit Account No. 12-1095 therefor.

Dated: May 15, 2002

Respectfully submitted,

By

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